

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: NEGISHI et al

Serial No.: 10/531,700

Filed: April 18, 2005

For: Method For Fabricating Semiconductor Device

Art Unit: 1765

Examiner: D. Deo

Conf. No.: 1389

**AMENDMENT**

Mail Stop: Amendment (Fee)  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 12, 2007

Sir:

The following amendments and remarks are respectfully submitted in connection with the above-identified application, in response to the office action dated June 12, 2007. The amendments are as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.